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International Application Number:		
Confirmation Number:	9338	
Title of Invention:	System and method for statistical timing analysis of digital circuits	
First Named Inventor/Applicant Name:	Chandramouli Visweswariah	
Correspondence Address:	Louis J. Percello Intellectual Property Law Dept. IBM Corporation P.O. Box 218 Yorktown Heights NY 10598 US 9149453145	
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